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002/004

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34756 7590 09/27/2006

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12/26/2006 TTRAN2 00000045 200782 10644621

01 FC:1501 1400.00 DA
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John C. Carey (Depositor's name)
December 22, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/644,621	08/19/2003	Ric Howard	065324-0028	7506

TITLE OF INVENTION: ASYNCHRONOUS, INDEPENDENT AND MULTIPLE PROCESS SHARED MEMORY SYSTEM IN AN ADAPTIVE COMPUTING ARCHITECTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700 \$1,400	\$300	\$0	\$1000	12/27/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
MCLEAN MAYO, KIMBERLY N	2187	711-147000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☒ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

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1 Patterson & Sheridan, L.L.P.

2 _____

3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

NVIDIA Corporation

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Santa Clara, CA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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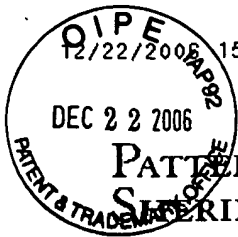
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Typed or printed name John C. Carey

Registration No. 51,530

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FACSIMILE COVER SHEET

DATE: December 22, 2006
FILE NO: NVDA/P002824
TO: Examiner: Kimberly N. McLean Mayo
MAIL STOP: ISSUE FEE
FAX NO: (571) 273-2885
COMPANY: USPTO
FROM: John C. Carey
PAGE(S) with cover: 4
ORIGINAL TO FOLLOW? ☐ YES ☒ NO

ISSUE FEE PAYMENT

TITLE: ASYNCHRONOUS, INDEPENDENT AND MULTIPLE PROCESS
SHARED MEMORY SYSTEM IN AN ADAPTIVE COMPUTING
ARCHITECTURE
U.S. SERIAL NO.: 10/644,621
FILING DATE: August 19, 2003
INVENTOR: Ric Howard
EXAMINER: Kimberly N. McLean Mayo
GROUP ART UNIT: 2187
CONFIRMATION NO.: 7506

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